

Electronic Acknowledgement Receipt

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Title of Invention:	Processor with scheduler architecture supporting multiple distinct scheduling algorithms
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		1150-1203_AM1.pdf	1217307 e90e46e26a85aaf11432b79ae6505d2bdf80e9a271	yes	14
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment - After Non-Final Rejection			1	1	
Specification			2	3	
Claims			4	8	
Applicant Arguments/Remarks Made in an Amendment			9	14	
Warnings:					
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